

ORGANIZING COMMITTEE

Chief Patron:

His Holiness Jagadguru Sri Shivarathri Deshikendra Mahaswamigalavaru
President, JSS Mahavidyapeetha, Mysuru

Patrons:

Prof. Vinay Kumar Pathak, Vice chancellor, AKTU, Lucknow
Dr. C.G Betsurmth, Executive Secretary, JSSMVP, Mysuru
Prof. M. H. Dhananjaya, JSSMVP, Mysuru
Dr. C. Ranganathaiah, JSSMVP, Mysuru
Dr. B.G. Sangameswara, JSSMVP, Mysuru

General Chair:

Prof. H.K. Paliwal, Dean of faculty, AKTU, Lucknow
Dr B. K. Murthy, Scientist G and Group Coordinator, Meity, Govt of India
Prof. T. N Nagabhushan, Principal, SJCE, Mysuru
Dr. Arti Noor, Joint Director, C-DAC, Noida.
Prof. V.K. Singh, BOS Convenor, AKTU, Lucknow
Prof. J.P. Saini, Director, NSIT, New Delhi
Prof. J.P. Pandey, Director, KNIT, Sultanpur.

Programme Chair:

Prof G .M. Patil, Principal, JSSATE, Noida.

Programme Coordinator:

Prof. Sampath Kumar V., Head of department
Department of Electronics Engineering, JSSATE, Noida and
Associate Dean (RDIC) AKTU, Lucknow

Departmental Organizing Committee

Mr. Shivaji Sinha, Assistant Professor, JSSATE, Noida
Ms. Gayatri Sakya, Assistant Professor, JSSATE, Noida
Ms. Ruchi Paliwal, Assistant Professor, JSSATE, Noida
Ms. Monika Malik, Assistant Professor, JSSATE, Noida

REGISTRATION DETAILS

Those willing to attend this FDP should send filled registration form to ngnscsecworkshop@gmail.com. Registration is free and will be on a first-come-first-served basis. Confirmation of registration will be informed by email. Accommodation and TA / DA will not be provided.

LAST DATE OF REGISTRATION

01st May, 2018



JSS MAHAVIDYAPEETHA
JSS ACADEMY OF TECHNICAL EDUCATION, NOIDA



FACULTY DEVELOPMENT PROGRAMME

On

Next Generation Nano Satellite Communication Systems

07th May – 11th May, 2018



Sponsored by:

**DR. A.P.J. ABDUL KALAM TECHNICAL UNIVERSITY
LUCKNOW**

Organized by:

**DEPARTMENT OF ELECTRONICS ENGINEERING
JSS ACADEMY OF TECHNICAL EDUCATION
C-20/1, SECTOR-62,
NOIDA-201301**

Ph: +91-0120-2400115, www.jssaten.ac.in



ABOUT THE COLLEGE

JSS Academy of Technical Education Noida (JSSATEN) is one of the leading Technical Institutions in the National Capital Region in the State of Uttar Pradesh. Established in the year 1998 by JSS Mahavidyapeetha, Noida, the Institution has set benchmarks every year, and grown into an Institution of Excellence in Technical Education. Located in the central part of NOIDA, JSSATEN has become a household name for its excellence in Discipline, Teaching, Training and Placement. The Institution has MOUs with Colorado Heights University, Denver, USA for student exchange program. IBM Centre of Excellence, Texas Instruments Centre of Excellence, Nokia Mobile Innovation Labs, e-Yantra - Embedded & Robotics Lab funded by MHRD under NMEICT, PLMCC, Schneider Electric Substation Automation Labs and Centre for Innovation & Design are additional facilities for the students to innovate new ideas. JSSATEN is proud to mention that it has created necessary manpower and infrastructure to implement **Outcome Based Education** from the year 2014-15. Today, JSSATEN has total student strength of over 4000, who are mentored by more than 250 Faculty Members. The Campus has finest accommodation for girls and boys.

<http://jssaten.ac.in/>

ABOUT THE DEPARTMENT

Electronics Engineering Department stands tall amongst other branches of engineering disciplines. Commissioned in the year 1998, to offer graduate level programme in 'Electronics and Communication Engineering', the Department has seen remarkable growth in terms of quality of student intake, inclusion of post-graduate programme 'Advance Electronics and Communication with specialization in VLSI Design' from 2008, applauding achievements of the students during and after graduation, faculty specialization and upgradation in several domain areas and infrastructure to provide the latest technical lab facilities e.g. Texas Instruments Centre of excellence and e-Yantra Laboratory Setup Initiative (eLSI) –IIT Bombay are unique facilities for research and project work, Entuple center of Excellence in the area of Microelectronics under cadence university programme. Department has consistently maintained an exemplary placement and academic record. B. Tech programme of Electronics and Communication Engineering accredited (Outcome Based Education) for two years by NBA from 1st July 2015. NBA approval program is a proof that department is committed to confidence and assurance on quality to all stake holders. Department has been identified as a research Centre in the field of electronics engineering for the technical university.

<http://jssaten.ac.in/Academics/ECE/>

AIM AND SCOPE OF THE FDP

This ONE week FDP on “Next Generation Nano Satellite Communication Systems” offers an expert views on availability of powerful, low cost and less weight

communication processing systems for the development of Nano satellites for low altitude applications. This workshop will provide faculty members with the most up-to-date technological developments in Nano and Micro Satellite communication systems. The main aim and

motivation for organizing this workshop is to give exposure to academicians and researchers in new trends of Satellite Communication systems.

This FDP proposes to build a strong faculty workforce in space technology for motivating students towards this area. Trained faculty will nurture the talented youth in the campuses to come out with new solutions for critical challenges in this domain. JSSATE, Noida is committed to educate all, in highly challenging and demanding technical areas. Department of Electronics Engineering has designed this program to support the kind of progressive teaching and learning that industry demands.

COURSE CONTENTS

- Inter-Nano Satellite Communications
- Distributed Nano satellite systems
- launching Technologies of Nano Satellites
- Nano satellites telemetry and command systems
- Antenna design in Nano satellites for inter-satellite links
- SDR challenges and Solution to Nano Satellite
- Earth Observation using Small Scale Nano Satellites
- Nano Satellite network technologies
- Routing techniques in Nano satellites
- Subsystem technologies for Nano satellites
- Flight opportunities and launchers for Nano satellites
- Imaging Trends in Micro/Nano Satellites
- Future Research trends in Nano Satellites

RESOURCE PERSONS

Eminent Professors and senior faculty members from IITs, IISc, NITs, as well as experts from industries and research centers i.e. ISRO, C-DoT, C-DAC, ALT, DST, DeitY etc. will deliver their talks during the various sessions of the programme.

PARTICIPANTS

- Faculty members of Engineering Colleges affiliated to AKTU and other Universities.
- Research scholars & PG students pursuing research / project work in this field.



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**FACULTY DEVELOPMENT PROGRAMME
On**

Next Generation Nano Satellite Communication Systems

**07th May - 11th May, 2018
REGISTRATION FORM**

Name:

Designation:

Department:.....

Institution / Organization.....

Area of Specialization:

Academic Qualification:.....

Address :.....
.....

E-mail:

Contact No.:

NO Registration Fee

Declaration

The above information furnished is correct to the best of my knowledge and belief.

Signature of Applicant

Signature of HOD

Sign. & Signature of Principal / Director