

ORGANIZING COMMITTEE

Chief Patron:

His Holiness Jagadguru Sri Shivarathri Deshikendra Mahaswamigalavaru
President, JSS Mahavidyapeetha, Mysuru

Patrons:

Prof. Vinay Kumar Pathak, Vice chancellor, AKTU, Lucknow
Dr. C.G Betsurmath, Executive Secretary, JSSMVP, Mysuru
Prof. M.H. Dhananjaya, JSSMVP, Mysuru
Dr. C. Ranganathaiah, JSSMVP, Mysuru
Dr. B.G. Sangameswara, JSSMVP, Mysuru

General Chair:

Prof. H.K. Paliwal, Dean of faculty, AKTU, Lucknow.
Dr. B.K. Murthy, Scientist G and Group Coordinator, Meity Govt. of India.
Prof. T. N. Nagbhusan, Principal, SJCE, Mysuru.
Dr. Arti Noor, Joint Director, C-DAC, Noida.
Prof. V.K. Singh, BOS Convenor, AKTU, Lucknow.
Prof. J.P. Saini, Director, NSIT, New Delhi.
Prof. J.P. Pandey, Director, KNIT, Sultanpur.

Program Chair:

Prof. G.M Patil, Principal, JSSATE, Noida.

Program Coordinator:

Prof. Sampath Kumar V., Head of Department,
Department of Electronics Engineering, JSSATE, Noida and
Associate Dean (RDIC), AKTU, Lucknow.

Departmental Organizing Committee:

Ms. Chhaya Grover, Associate Professor, JSSATE, Noida.
Ms. Sangeeta Mangesh, Assistant Professor, JSSATE, Noida.
Mr. Varun Saxena, Assistant Professor, JSSATE, Noida.

REGISTRATION DETAILS

Those willing to attend this FDP should send filled registration form to ecworkshop@jssaten.ac.in. Registration is free and will be on a first-come-first-served basis. Confirmation of registration will be informed by email. Accommodation and TA / DA will not be provided.

LAST DATE OF REGISTRATION :

20th April 2018



JSS MAHAVIDYAPEETHA
JSS ACADEMY OF TECHNICAL EDUCATION, NOIDA



FACULTY DEVELOPMENT PROGRAMME

On

Signal Processing and Its Applications

23rd April – 28th April, 2018



Sponsored by

DR.APJ ABDUL KALAM TECHNICAL UNIVERSITY,
LUCKNOW.

Organized by

DEPARTMENT OF ELECTRONICS ENGINEERING
JSS ACADEMY OF TECHNICAL EDUCATION

C-20/1, Sector 62, Noida.

Ph: +91-0120-2400115 www.jssaten.ac.in



ABOUT THE COLLEGE

JSS Academy of Technical Education Noida (JSSATEN) is one of the leading Technical Institutions in the National Capital Region in the State of Uttar Pradesh. Established in the year 1998 by JSS Mahavidyapeetha, Noida, the Institution has set benchmarks every year, and grown into an Institution of Excellence in Technical Education. Located in the central part of NOIDA, JSSATEN has become a household name for its excellence in Discipline, Teaching, Training and Placement. The Institution has MOUs with Colorado Heights University, Denver, USA for student exchange program. IBM Centre of Excellence, Texas Instruments Centre of Excellence, Nokia Mobile Innovation Labs, e-Yantra - Embedded & Robotics Lab funded by MHRD under NMEICT, PLMCC, Schneider Electric Substation Automation Labs and Centre for Innovation & Design are additional facilities for the students to innovate new ideas. JSSATEN is proud to mention that it has created necessary manpower and infrastructure to implement **Outcome Based Education** from the year 2014-15. Today, JSSATEN has total student strength of over 4000, who are mentored by more than 250 Faculty Members. The Campus has finest accommodation for girls and boys. <http://jssaten.ac.in/>

ABOUT THE DEPARTMENT

Electronics Engineering Department stands tall amongst other branches of engineering disciplines. Commissioned in the year 1998, to offer graduate level programme in 'Electronics and Communication Engineering', the Department has seen remarkable growth in terms of quality of student intake, inclusion of post-graduate programme Advance Electronics and Communication with specialization in VLSI Design' from 2008, applauding achievements of the students during and after graduation, faculty specialization and upgradation in several domain areas and infrastructure to provide the latest technical lab facilities e.g. Texas Instruments Centre of excellence and e-Yantra Laboratory Setup Initiative (eLSI) –IIT Bombay are unique facilities for research and project work, Entuple center of Excellence in the area of Microelectronics under cadence university programme. Department has consistently maintained an exemplary placement and academic record. B. Tech programme of Electronics and Communication Engineering accredited (Outcome Based Education) for two years by NBA from 1st July 2015. NBA approval program is a proof that department is committed to confidence and assurance on quality to all stake holders. Department has been identified as a research Centre in the field of electronics engineering for the technical university.

<http://jssaten.ac.in/Academics/ECE/>

AIM AND SCOPE OF THE FDP

This Faculty Development Program is focused to discuss state-of-the-art developments and emerging techniques in signal processing application domains ranging from Biomedical Signal Processing, Audio/Speech Signal Processing and Signal Processing for Telecommunication and Hands-on Laboratory Sessions from Industry professionals. The workshop will provide an opportunity to faculty members and on-going researchers to equip and advance their knowledge in these application areas and also, identify the prospective areas for novel research.

COURSE CONTENTS

- Biomedical Signal Processing.
- Signal Processing for Audio/Speech Signal.
- Signal Processing Techniques for 4G & 5G.
- Sensor Array and Multi-Channel Signal Processing
- Soft Computing for signal Processing
- Machine Learning for signal Processing
- Statistical Signal Processing
- VLSI Signal Processing
- Mixed Signal Modeling
- Signal Processing for IOT.
- Radar Signal Processing
- Intelligent Signal Processing & Control System Design
-

RESOURCE PERSONS

Eminent Professors and senior faculty members from IITs, IIITs, NITs, as well as experts from industries and research centers such as C-DAC, DST, DeitY etc. will be engaging sessions during the workshop.

PARTICIPANTS

Faculty members of Engineering Colleges affiliated to AKTU and other Universities.

Research scholars & PG students pursuing research / project work in this field.



JSS MAHAVIDYAPEETHA
JSS ACADEMY OF TECHNICAL EDUCATION, NOIDA



FACULTY DEVELOPMENT PROGRAMME

On

Signal Processing and Its Applications

23rd April - 28th April, 2018

REGISTRATION FORM

Name:

Designation:

Department:.....

Institution / Organization.....

Area of Specialization:

Academic Qualification:.....

Address :.....

.....

E-mail:

Contact No.:

NO Registration Fee

Declaration

The above information furnished is correct to the best of my knowledge and belief.

Signature of Applicant

Signature of HOD

Sign. & Signature of Principal / Director